



DEVSEL: 16 bytes I/O space at \$C0x0-\$C0xF (x=n+9)
 IOSEL: 256 bytes memory at \$Cn00-\$CnFF
 IOSTRB: 2K memory at \$C800-\$CFFF

U3C SCTRL

A2D	2	I1	I/O0	12	SCDACK
A1D	3	I2	I/O1	13	SCWR
A0D	4	I3	I/O2	14	SCWR
DEVSEL	5	I4	I/O3	15	SCCS
RWB	7	I5	I/O4	16	UNK2
TWCLK	8	I6	I/O5	17	UNK2
RST	9	I8	I/O6	18	FFSET
A3D	1	I9	I/O7	19	FFSET
		I0	CLK	10	16V8
		I1	OE	11	16V8

SCCS = DEVSEL * I3D
 SCWR = SCCS * RWB
 SCWR = TWCLK * RWB

U3D

IOSTRB	2	I14	I/O0	12	ROMOE
IOSEL	3	I2	I/O1	13	ROMCS
RST	4	I3	I/O2	14	RAMCS
A10R	5	I4	I/O3	15	BD7
UNK2	6	I5	I/O4	16	FFCLK
RWB	7	I6	I/O5	17	UNK1
FFSET	8	I7	I/O6	18	DEVSEL
SCDRQ	9	I8	I/O7	19	DE

MEM ENABLE LATCH
 Set on IOSEL
 Reset on IOSTRB & ROM_DIS

ROMCS = MEM_EN * IOSTR * I10R
 ROMOE = ROMCS * IRWB
 RAMCS = MEM_EN * IOSTR * A10R
 DE = DEVSEL + IOSEL + (IOSTR * MEM_EN)
 BD7_OE = DEVSEL * DRQ_EN * IRWB
 BD7 = SCDRQ

Decodes I/O space 8..F

A0D	1	A0	00	15	SA_EN
A1D	2	A1	01	14	BANK_SEL
A2D	3	A2	02	13	SS_RES
		A3	03	12	
		A4	04	11	
		A5	05	10	
		A6	06	9	
		A7	07	8	
		A8	08	7	
		A9	09	6	
		A10	0A	5	
		A11	0B	4	
		A12	0C	3	
		A13	0D	2	
		A14	0E	1	
		A15	0F	0	

8: PDMA/DACK
 9: SCSI ID
 A: BANK SELCT
 B: Reset 5380
 C: RSVD
 D: PDMA Enable
 E: Read DRQ on D7
 F: RSVD