BUS'APPLE2_SLOT

DEVSEL: 16 bytes I/O space at $C0x0-$C0xF (x=n+9)
IOSEL: 256 bytes memory at $Cn00-$CnFF
IOSTRB: 2K memory at $C800-$CFFF

U4 74LS245

U7 6264

U8 COMMON 27C128

RP1 COMMON 10K

Mapped in I/O space 0..7

U19 5380

J1 CONN_13X2

U16A U16B 74LS74

U11A U11B U11C U11D 74LS09

U3C SCTRL U3 COMMON 16V8

SCCS = DEVSEL * !A3D
SCRD = SCCS * RWB
SCWR = TWCLK * RWB

U14 COMMON 16V8

A10=0 ROM, A10=1 RAM

U3D

MEM ENABLE LATCH
Set on IOSEL
Reset on IOSTRB & ROM_DIS

ROMCS = MEM_EN * IOSTR * !A10R
ROMOE = ROMCS * !RWB
RAMCS = !MEM_EN * IOSTR * A10R
DE = DEVSEL + IOSEL + (IOSTR * MEM_EN)
BD7.OE= DEVSEL * DRQ_EN * !RWB
BD7=SCDRQ

U2 74LS138

Decodes I/O space 8..F

B: PDMA/DACK
9: SCSI ID
A: BANK SELCT
B: Reset 5380
C: RSVD
D: PDMA Enable
E: Read DRQ on D7
F: RSVD

U15 74LS273

BANK REGISTER: bit 0..3 ROM Addr, 4..6 RAM Addr, 7 RSVD

U18 TERM UC5606

U19 TERM UC5606

U17 74LS240

RP2 COMMON 10K

GGLABS	Sheet: / File: a2scsi.sch		
	A2SCSI – Apple II SCSI Card		
Size: A2	Date: 2019-01-19		Rev: A00
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