



DEVSEL: 16 bytes I/O space at \$C0x0-\$C0xF (x=n+9)  
 IOSEL: 256 bytes memory at \$Cn00-\$CnFF  
 IOSTRB: 2K memory at \$C800-\$CFFF

U3C SCTRL  
 A2D 2 I1 I/O0 12 SCDACK  
 A1D 3 I2 I/O1 13 SCWR  
 AOD 4 I3 I/O2 14 SCWR  
 DEVSEL 5 I4 I/O3 15 SCCS  
 RWB 7 I6 I/O5 17 UNK2  
 TWCLK 8 I7 I/O6 18 FFSET  
 RST 9 I8 I/O7 19 FFSET  
 A3D 1 I0/CLK I9/DE 16V8

SCCS = DEVSEL \* A3D  
 SCRD = SCCS \* RWB  
 SCWR = TWCLK \* RWB

U3D  
 IOSEL 2 I1 I/O0 12 ROMOE  
 RST 3 I2 I/O1 13 ROMCS  
 A10R 4 I3 I/O2 14 RAMCS  
 A10R 5 I4 I/O3 15 B07  
 RWB 7 I6 I/O5 16 FFCLK  
 FFSET 8 I7 I/O6 17 UNK1  
 SCDRQ 9 I8 I/O7 18 DEVSEL  
 ROM\_DIS 1 I0/CLK I9/DE 16V8  
 DRQ\_EN 11

MEM ENABLE LATCH  
 Set on IOSEL  
 Reset on IOSTRB & ROM\_DIS

ROMCS = MEM\_EN \* IOSTR \* A10R  
 ROMOE = ROMCS \* IRWB  
 RAMCS = MEM\_EN \* IOSTR \* A10R  
 DE = DEVSEL + IOSEL + (IOSTR \* MEM\_EN)  
 BD7.OE = DEVSEL \* DRQ\_EN \* IRWB  
 BD7 = SCDRQ

Decodes I/O space 8..F  
 A0D 1 A0 00 15 SA\_EN  
 A1D 2 A1 01 14 BANK\_SEL  
 A2D 3 A2 02 13 SS\_RES  
 A3D 6 E3 05 10 X  
 DEVSEL 4 E2 06 9 X  
 E1 07 7 X